

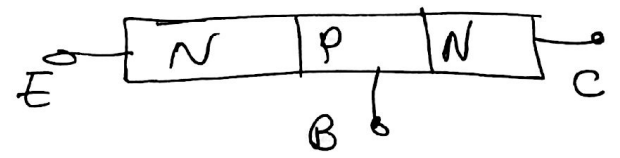
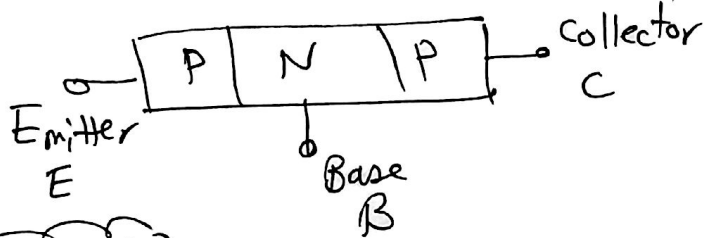
BJT Transistor Lec (13-16)

* Bjt \rightarrow Bipolar junction transistor [2 PN junction connected Back to Back] \rightarrow It has 3 terminal device & has 3 set of Voltage-current characteristics.

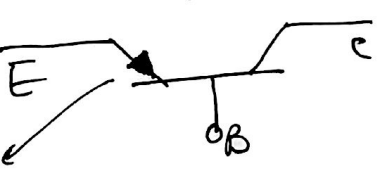
* Bjt used to perform amplifications for $\begin{matrix} \rightarrow V \\ \rightarrow I \\ \rightarrow P \end{matrix}$
 (holes \rightarrow 2 carriers, \rightarrow ~~electron~~ \rightarrow Bi)

Operation

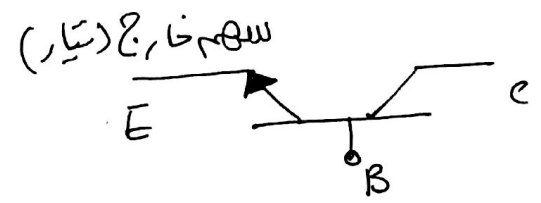
\rightarrow a semiconductor layer (N, or P) between two other type layers (N between 2P or P between 2N)



Symbol

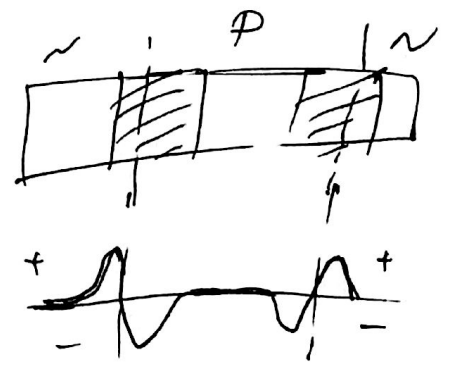
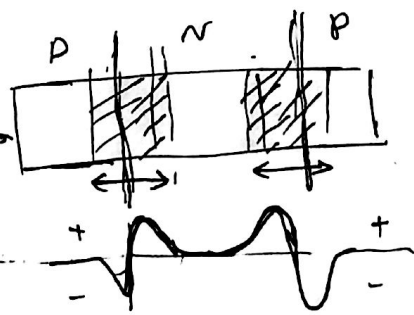


(ج, ب, ع) \rightarrow Conventional current

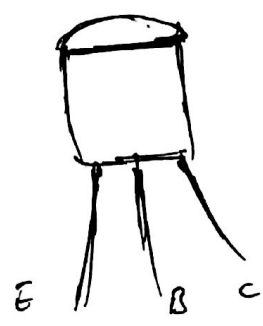


Barrier Potential

\rightarrow Barrier potential is +ve on N side & -ve on P side



Switch \rightarrow Tr. \rightarrow لا يوصل التيار
 amplifier \rightarrow \rightarrow Basic \rightarrow \rightarrow operation



operation (cont.)

→ There are 2 PN junction in BJT
 (Collector-Base junction, Emitter Base junction (EB))
 CB

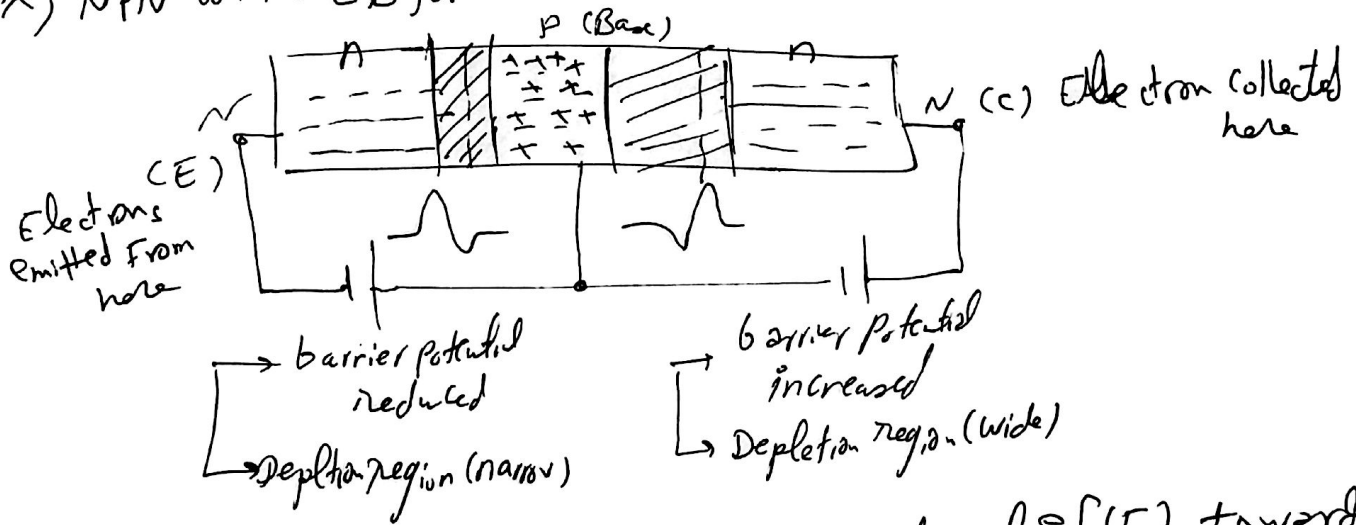
→ to operate as amplifier, (EB must be forward & BC Reverse)

→ The middle layer (Base) → very thin (narrow) than the
 Two layers (more lightly doped)

• Because of lightly doped layer ∴ Conductivity of Base (decreased)
 and so, the resistance (increased), the distance betⁿ CB & EB
 is minimized & free electrons

• The Barrier Potential are +ve in n-type & -ve in p-type

EX) NPN WITH EB forward & CB reverse



→ electrons emitted from (E) and leave n-material = f(E) towards
 p material (Base) & move easily because depletion region is narrow
 between EB & potential barrier is reduced.

→ when arrive PN junction (BC), the electrons move forward rapidly
 towards collector (because of positive supply voltage = V_{CC})
 collector \rightarrow V_{CC}

Transistor current

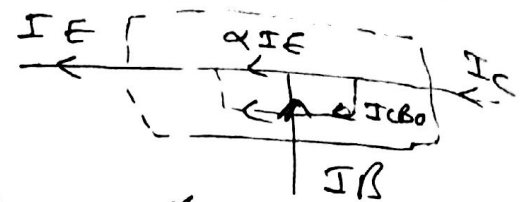
- ∴ BE junction is Forward Bias → depletion region is reduced due to reduction of Potential Barrier, BC is Reverse
⇒ large no. of majority carriers (Electrons) diffuse across BE junction (Forward) → These electrons enter the (Base region) and will have 2 choices.

- ↳ Exit to the Voltage source through Base terminal (X) in this case zero
- ↳ Continue flowing to the Collector across (wide depletion region (Because the positive voltage across collector layer > voltage of Base])

- Since the Base region is lightly doped (high resistance) & " " " " is Thin, the electrons need to travel less distance to be attracted to the high positive potential of collector potential terminal,

- But (small amount of electrons leave Base through the source connection, while the Big (major) amount of current flow into Collector.

- The current is result from (minority carrier in P (E) + Electron, emitted from emitter & reaches P (Base))



1. $I_C = \alpha I_E + I_{CBO}$

↳ Reverse current
↳ Portion of emitter current

$\alpha \rightarrow$ Common Base Current gain = $\frac{\Delta I_C}{\Delta I_E} \Big|_{V_{CB} \text{ const.}}$

$I_{CBO} \rightarrow$ Leakage Current due to Reverse Bias between (B & C)

2. $I_E = I_C + I_B$

Solve 1 & 2 $I_B = I_E - I_C = \frac{I_C - I_{CBO}}{\alpha} - I_C = (1 - \frac{\alpha}{\alpha}) I_C - \frac{I_{CBO}}{\alpha}$

3. $\beta = \text{current gain} = (\frac{\Delta I_C}{\Delta I_B}) = \frac{\alpha}{1 - \alpha}$

4. $I_B = \frac{I_C}{\beta} - \frac{I_{CBO}}{\alpha}$

Note I_{CBO} very small value [can be neglected]

\therefore after neglecting I_{CBO}

- ① $I_C \cong \alpha I_E$
- ② $I_E = I_C + I_B$
- ③ $I_C = \beta I_B$
- ④ $\beta = \frac{\alpha}{1 - \alpha}$

Ex) $\alpha = 0.9 \rightarrow 0.998$

$\beta = 20 \rightarrow 800$

EX(1)

Calculate the value of Collector current & Emitter current for transistor with $\alpha_{dc} = 0.98$ & $I_{CBO} = 5\mu A$ & $I_B = 10\mu A$

Sol

$$I_C = \alpha I_E + I_{CBO}$$

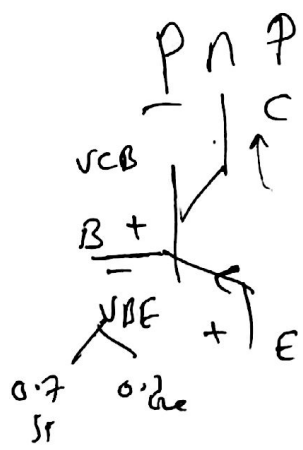
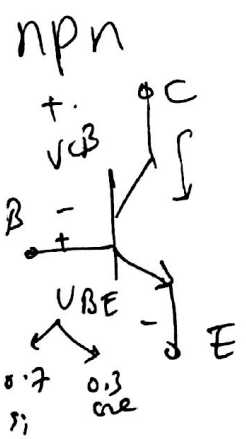
$$I_E = \alpha [I_C + I_B] + I_{CBO}$$

$$\therefore I_C = 0.98 I_C + 0.98 \times 10 \times 10^{-6} + 5 \times 10^{-6}$$

$$0.02 I_C = 103 \times 10^{-6}$$

$$\therefore I_C = 5.15 \text{ mA}$$

* Transistor Voltage *



V_{BE} (Si) 0.7 = V_{BE}
 V_{BE} (Ge) 0.3

EX(2)

The Collector & Base currents of transistor are measured as

$I_C = 5.202 \text{ mA}$, $I_B = 50 \mu A$
 $I_{CBO} = 2 \mu A$, calculate (a) α_{dc}
 (b) The new I_B value that make $I_C = 10 \text{ mA}$

Sol

(a) $I_C = \alpha I_E + I_{CBO}$

$$5.202 \times 10^{-3} = \alpha [I_C + I_B] + 2 \times 10^{-6}$$

$$5.202 \times 10^{-3} = \alpha [5.202 \times 10^{-3} + 50 \times 10^{-6}] + 2 \times 10^{-6}$$

$$\therefore \alpha = 0.9999$$

$$\beta = \frac{\alpha}{1-\alpha} = \frac{0.9999}{1-0.9999} \approx 100$$

(b) $I_C = 10 \text{ mA}$

$$10 \times 10^{-3} = 0.99 [10 \times 10^{-3} + I_B] + 2 \times 10^{-6}$$

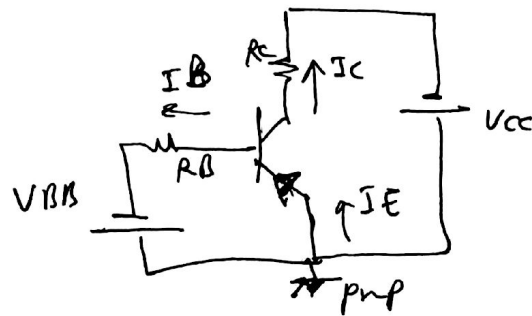
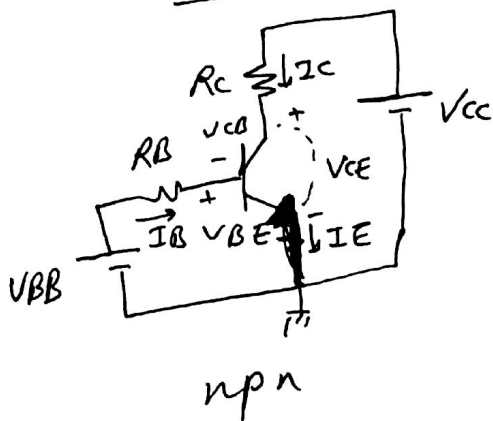
$$\therefore I_B = 98 \mu A$$

الجهد المبني للتحيز للترانزستور

فيها خط اس قبة

Transistor characteristics and parameters

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* V_{BE} Forward biased (Base emitter) junction
 V_{CC} Reverse " (Base collector) junction

* when (BE) forward \rightarrow like Diode forward with voltage drop (0.7V)

① $V_{BE} = 0.7$

② * $I_B = \frac{V_{BB} - V_{BE}}{R_B}$

③ $V_{CE} = V_{CC} - I_C R_C$

④ $V_{CB} = V_{CE} - V_{BE}$

There are 6 parameters
 I_C, I_B, I_E
 V_{BE}, V_{CE}, V_{CB}
 $I_{CBO} (\beta, V_{BB}, V_{CC} \text{ side})$

EX(3) if $R_C = 100\Omega, R_B = 10k\Omega, V_{CC} = 10V, V_{BB} = 5V, \beta = 150$
 find $I_B, I_C, I_E, V_{BE}, V_{CE}$ and V_{CB}

Sol // ① $V_{BE} = 0.7$

② $I_B = \frac{V_{BB} - V_{BE}}{R_B} = \frac{5 - 0.7}{10k} = 430\mu A$

③ $I_C = \beta I_B = 150 \times 430\mu A = 64.5mA$

④ $I_E = I_C + I_B = 64.5mA + 430\mu A = 64.93mA$

⑤ $V_{CE} = V_{CC} - I_C R_C = 10 - (64.5 \times 10^{-2} \times 100) = 3.55V$

⑥ $V_{CB} = V_{CE} - V_{BE} = 3.55 - 0.7 = 2.85V$

Solve yourself: $R_B = 22k, R_C = 220\Omega, V_{BB} = 6V, V_{CC} = 9V, \beta = 90$
 Find $I_B, I_C, I_E, V_{CE}, V_{CB}$ Ans. $[0.241mA / 21.68mA / 21.92mA$
 $4.23V / 3.53V]$